Ministry of high Education and Scientific Research Foundation of Technical Education Technical Institute of kufa

#### **Power Electronics**

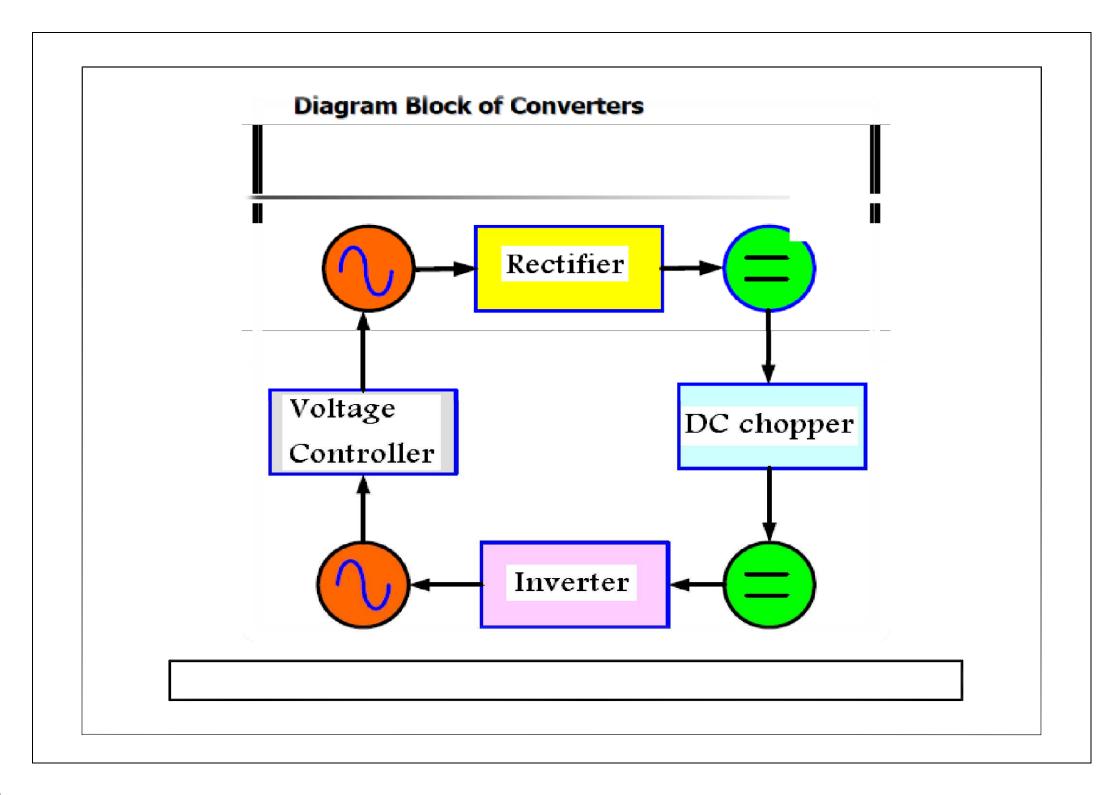
For Students of second class electrical department By jaafer sadiq jaafer Lecturer in electrical Dep.

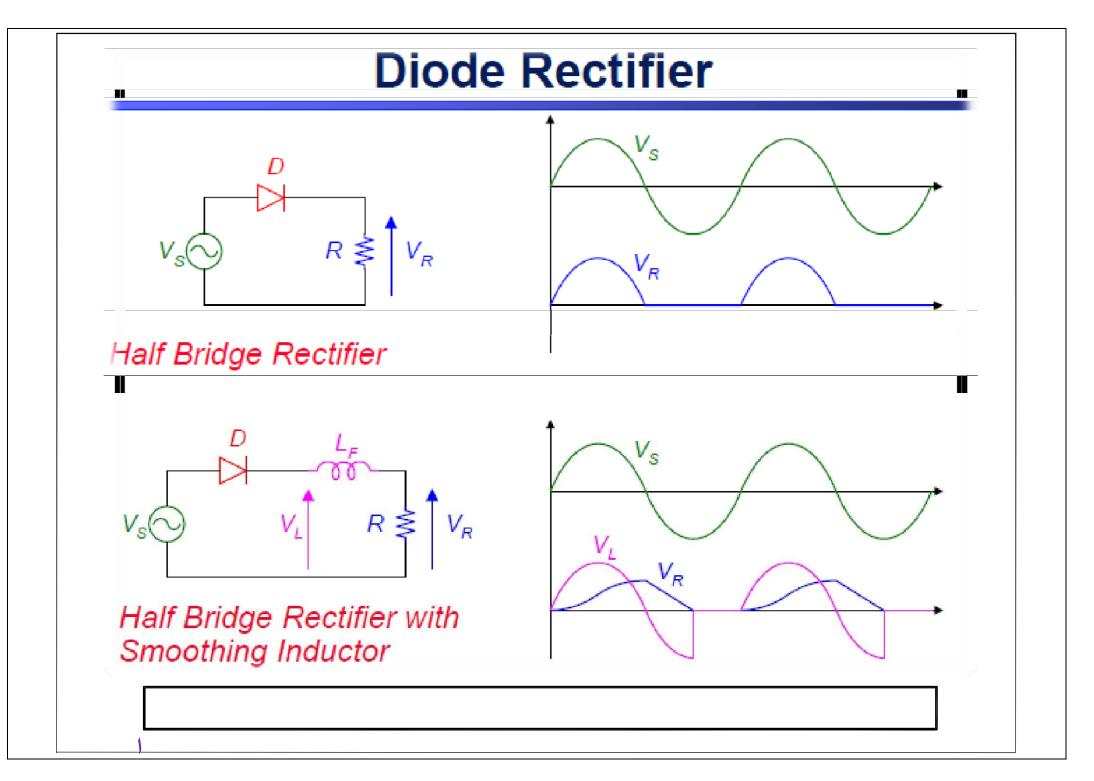
# Week No -1

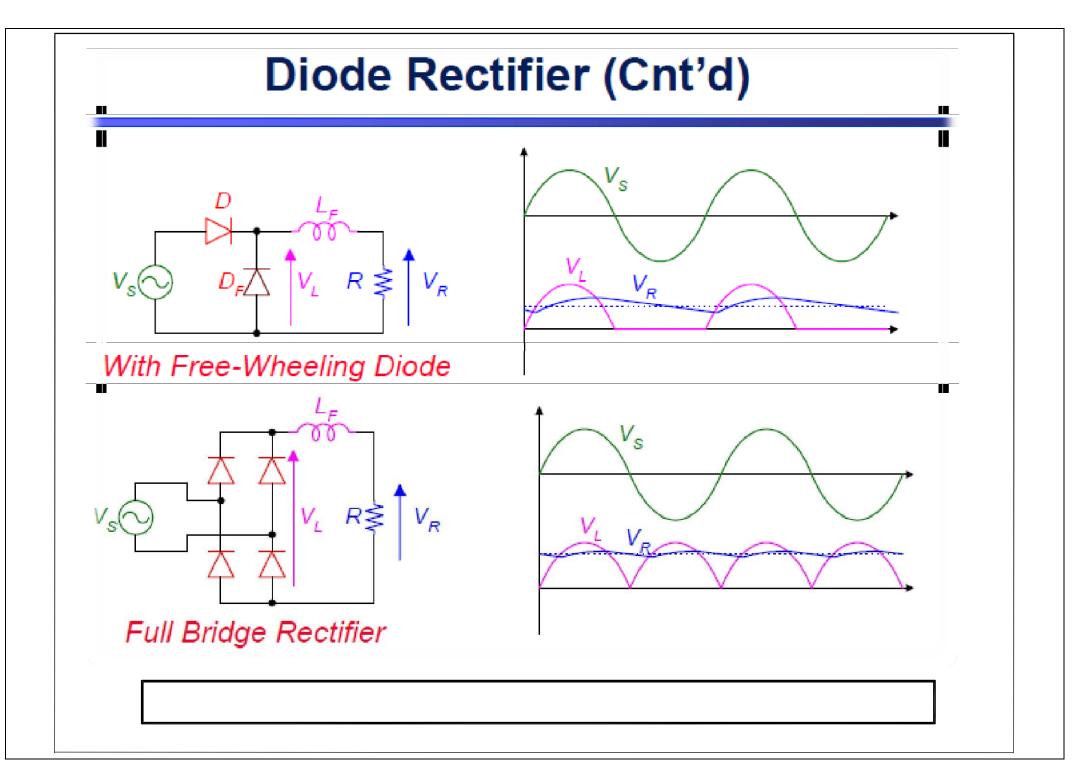
- Power electronic
- Electronic components used in power electronics
  - Power Diode
  - Power transistor
  - Thyristor
- Prevision of single phase rectifier circuits by using diode

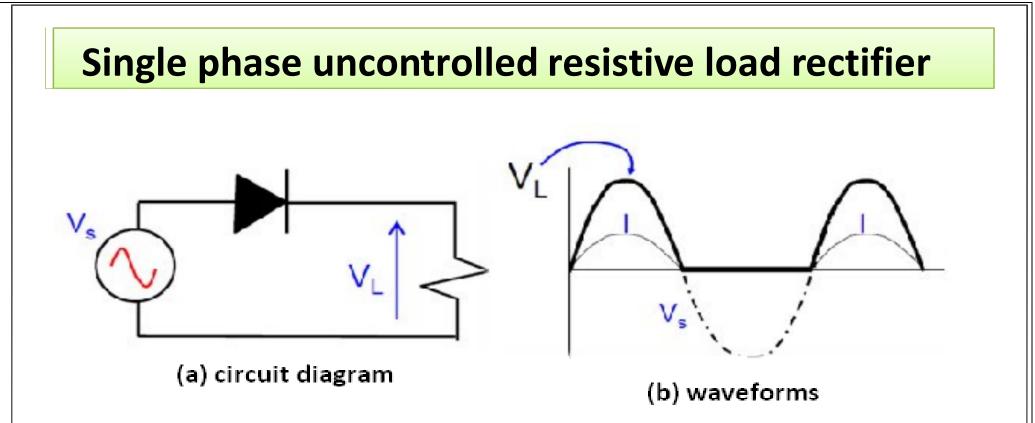
# What is Power Electronics ?

- Conversion of *Large Amounts of* Electric Power and Energy
  - ac to dc
  - dc to ac
  - dc to dc
  - ac to ac



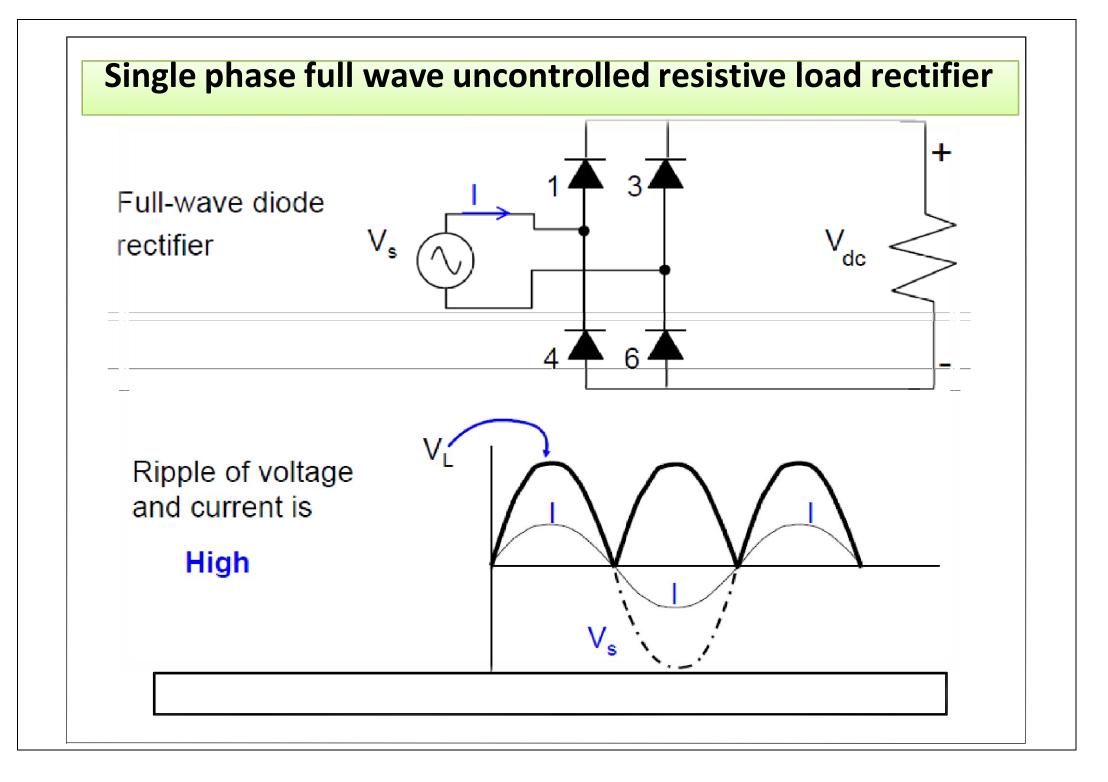






The main disadvantages of half wave rectifier are:

- 1- High ripple factor,
- 2-Low rectification efficiency,
- 3- Low transformer utilization factor, and,
- 4- DC saturation of transformer secondary winding.



#### **Performance Parameters**

- Average value of the output voltage, V<sub>dc</sub>
- Average value of the output current, I<sub>dc</sub>
- Output dc power, P<sub>dc</sub>
  - $-P_{dc} = V_{dc}I_{dc}$
- rms value of the input voltage, V<sub>rms</sub>
- Output ac power, P<sub>ac</sub>

$$-\mathbf{P}_{ac} = \mathbf{V}_{rms}\mathbf{I}_{rms}$$

# **Performance Parameters (continued)**

• Efficiency ,  $\eta$ 

$$\eta = P_{dc} / P_{ac}$$

- Effective (rms) value of the ac component of the output voltage,  $V_{ac}$ 

$$V_{ac} = \sqrt{V_{rms}^2 - V_{dc}^2}$$

• Form factor, FF

$$FF = V_{rms} / V_{dc}$$

• Ripple factor, RF

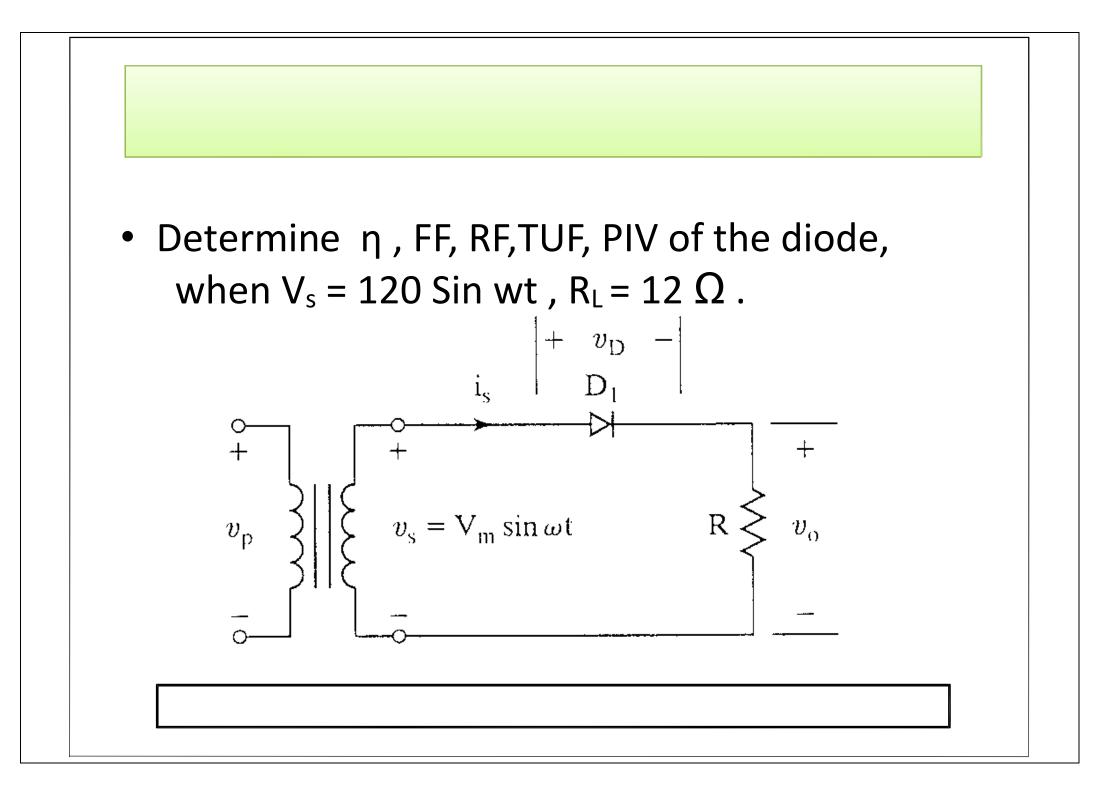
$$RF = V_{ac} / V_{dc}$$

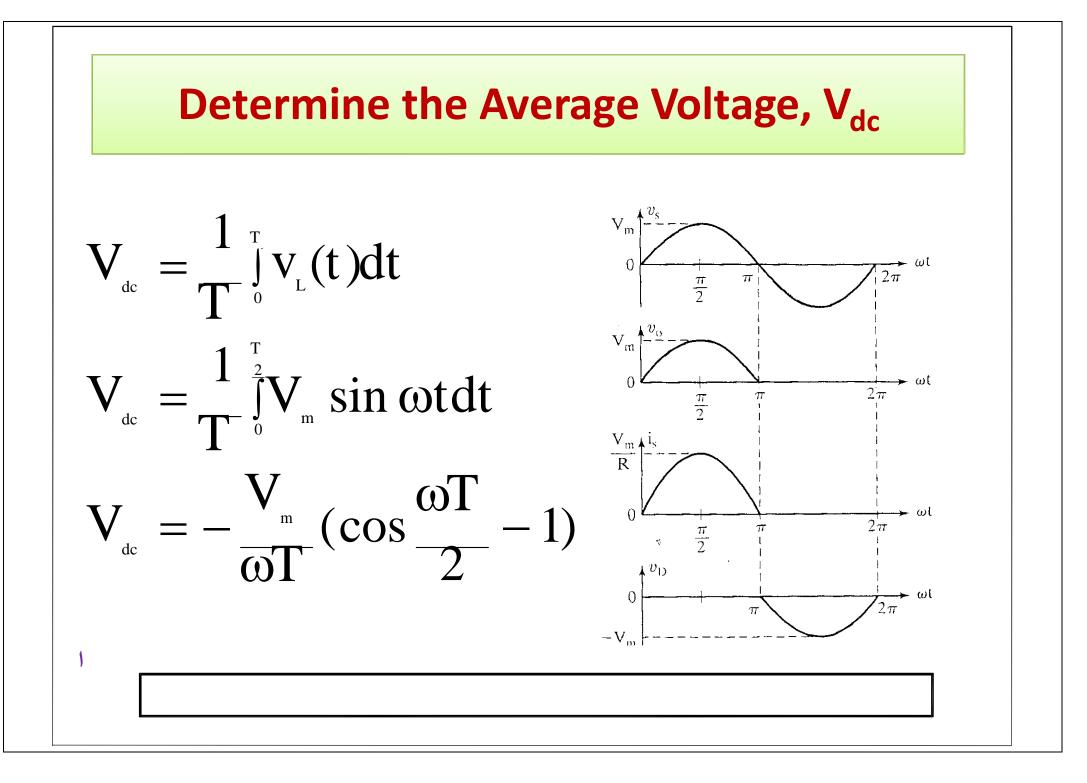
#### **Performance Parameters**

• Alternate form for ripple factor

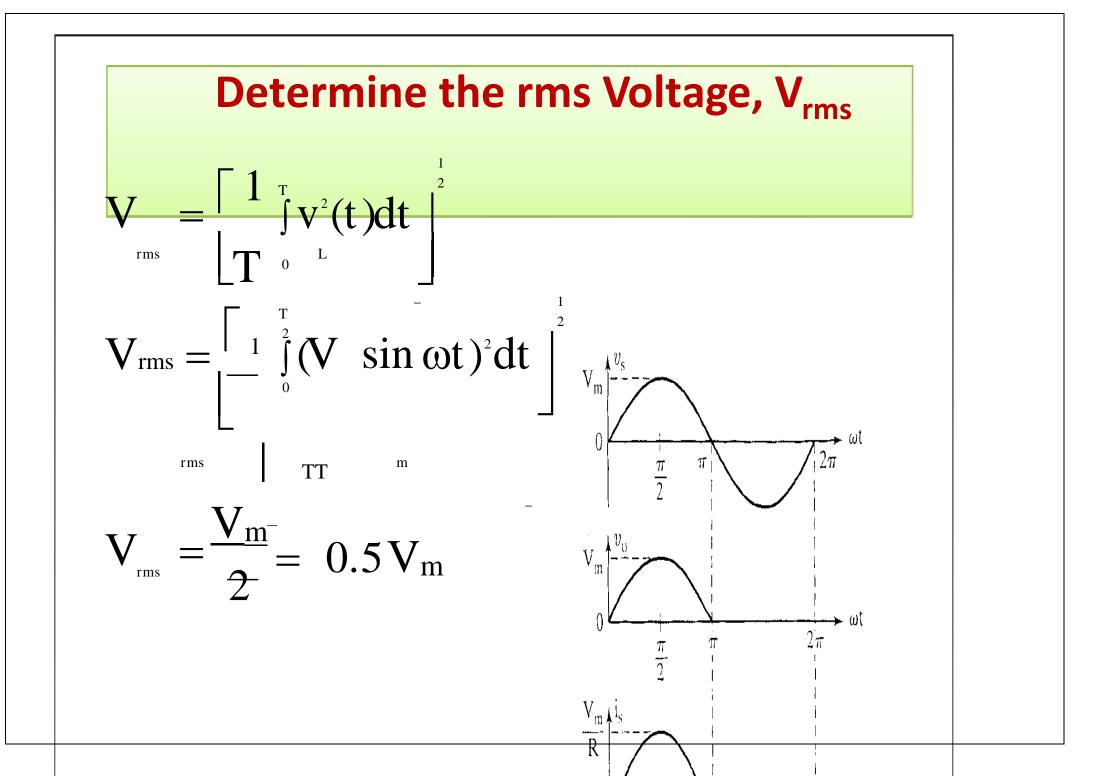
$$RF = \sqrt{(\frac{V_{rms}}{V_{dc}})^2} - 1 = \sqrt{FF^2 - 1}$$

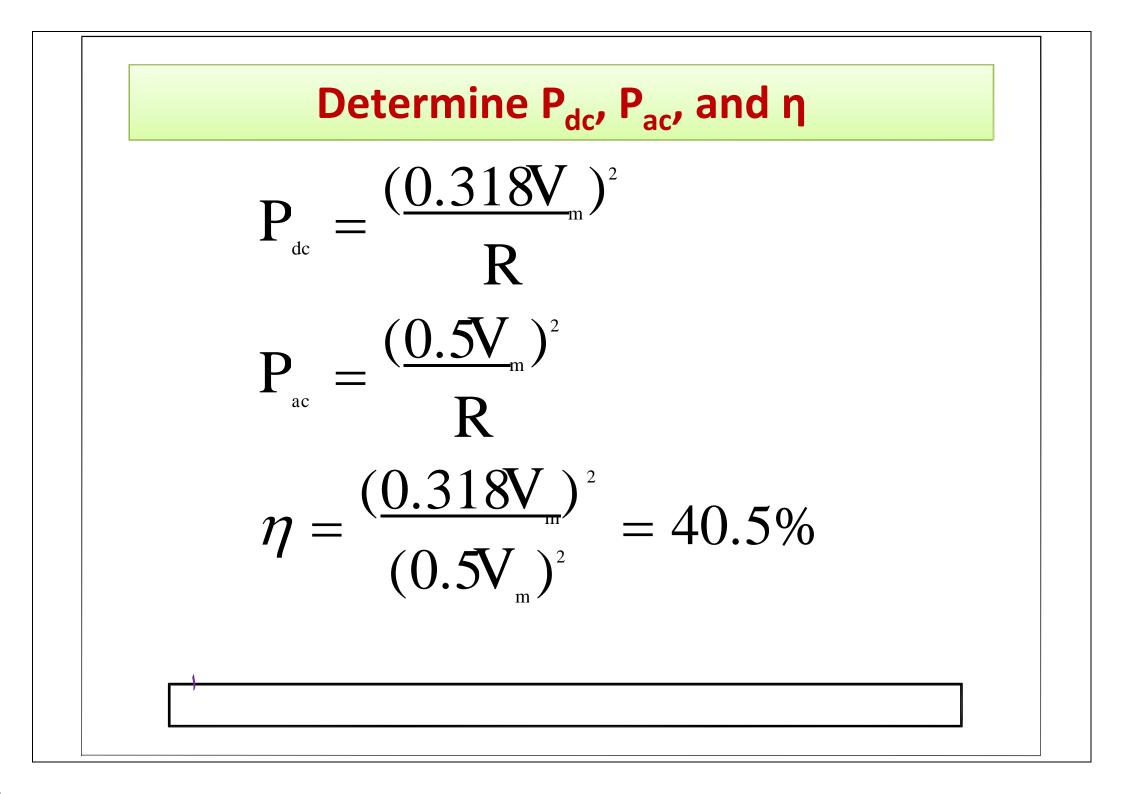
- Transformer utilization factor, TUF TUF =  $P_{dc} / V_s I_s$  $V_s$ ,  $I_s$  are rms voltage and current of the
  - transformer secondary





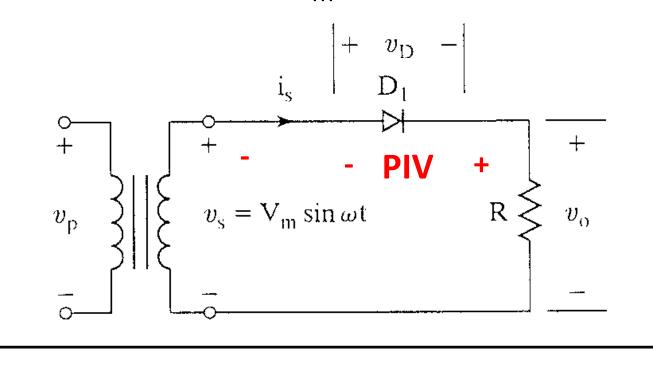
$$f = \frac{1}{T}$$
$$\omega = 2\pi f$$
$$V_{dc} = \frac{V_{m}}{\pi} = 0.318 Vm$$

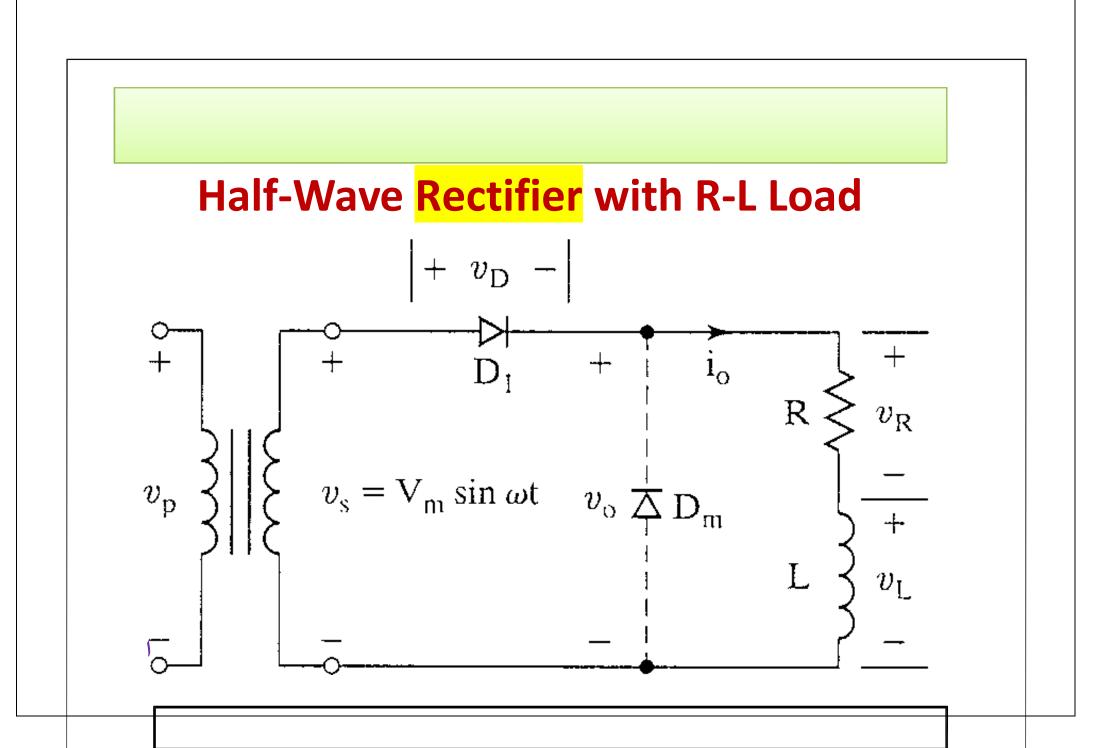


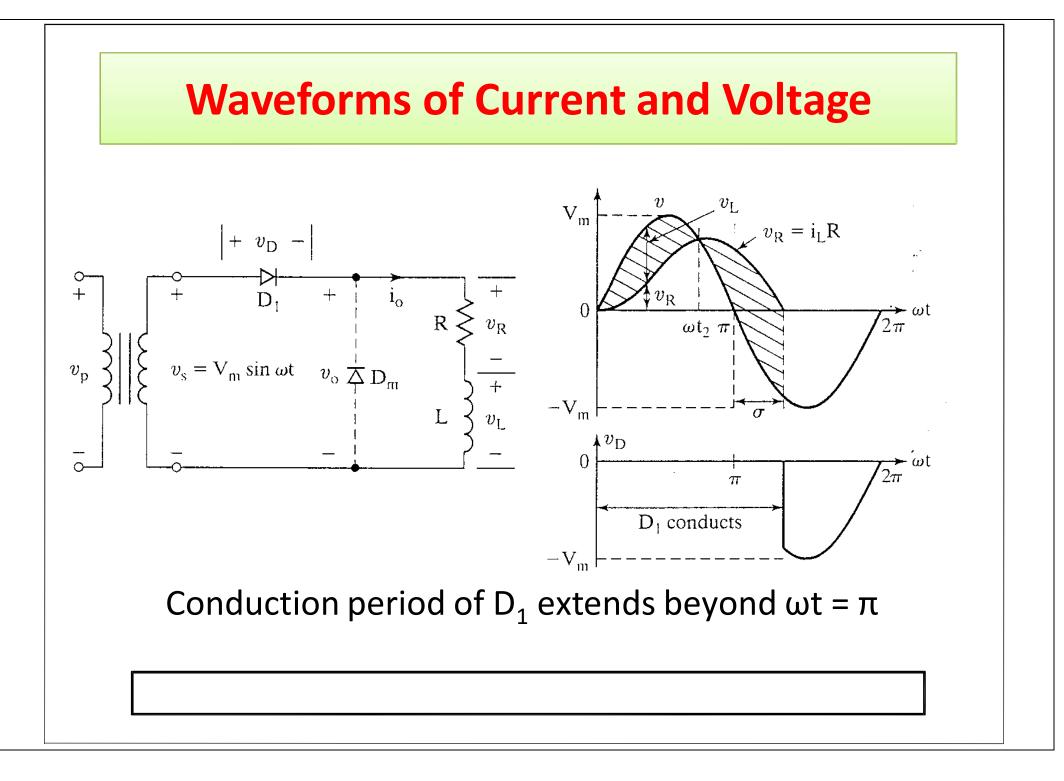


#### **Determine the PIV**

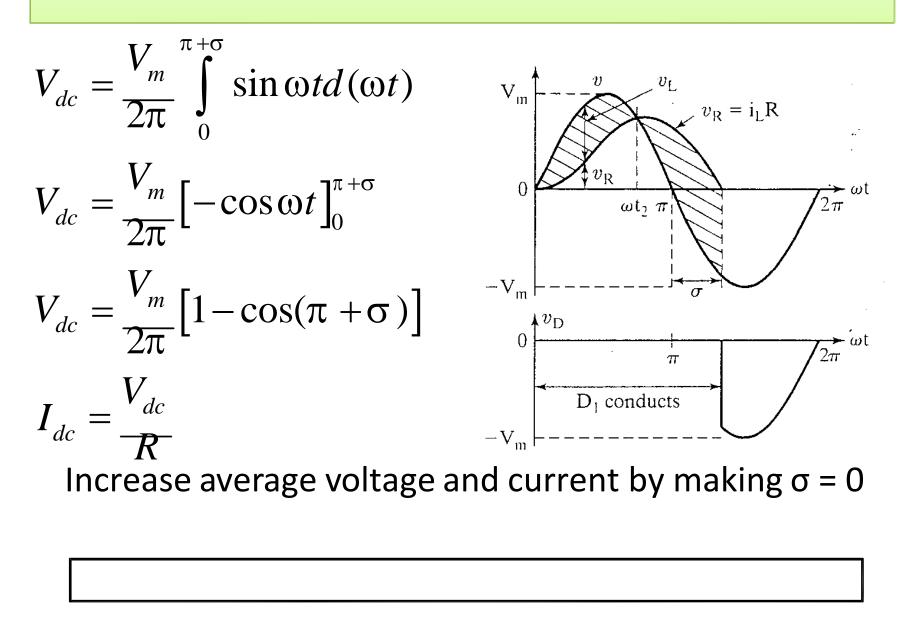
• PIV is the maximum (peak) voltage that appears across the diode when reverse biased. Here, PIV =  $V_m$ .







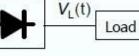
#### **Average Output Voltage**



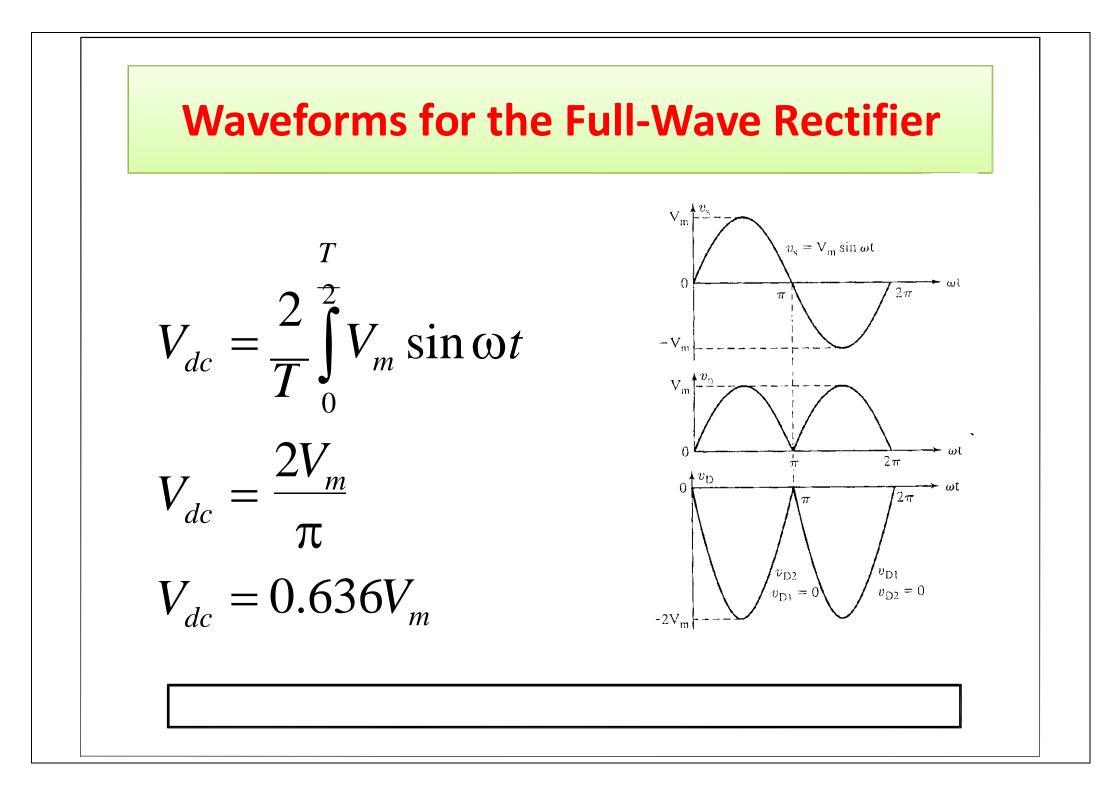
### Comparison among topologies 1-Ph.

- Secondary voltage is sinusoidal: v<sub>s</sub>(t) = V<sub>s</sub> sin (2πf<sub>mains</sub>t)
- Resistive Load
- Ideal devices (no device losses)

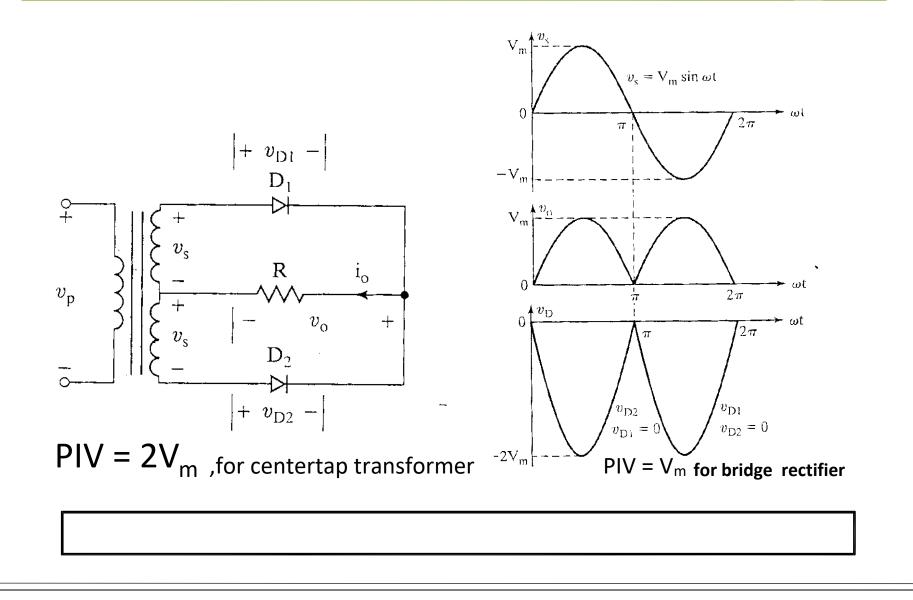
$V_{p}(t)$	$\cap$	$\sim$	Vs	(t)	N
	C	L			T

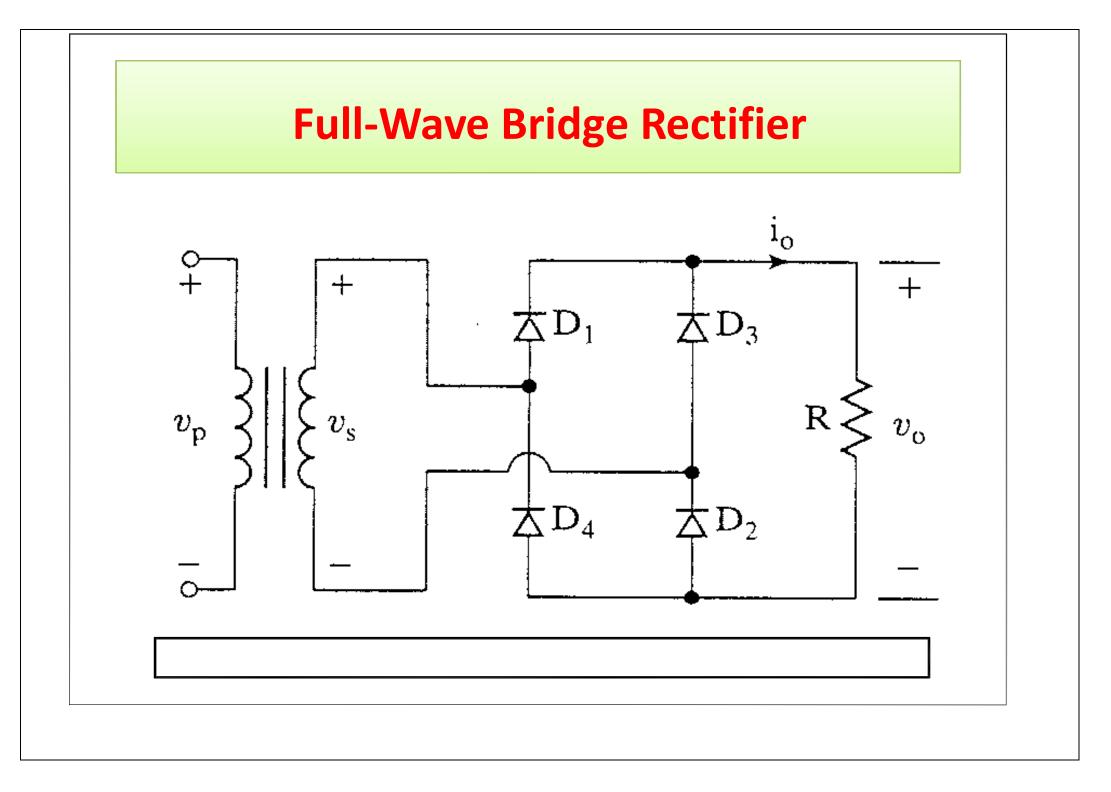


Parameter	Half-Wave	Full - Wave (Center-tapped)	Full - Wave (Bridge)
Rectified Voltage - V <sub>DC</sub>	V <sub>s</sub> /π = 0.318 ·V <sub>s</sub>	2 ·V <sub>s</sub> /π = 0.636 ·V <sub>s</sub>	2 ·V <sub>s</sub> /π = 0.636 ·V <sub>s</sub>
rms Output Voltage - V <sub>L</sub>	V <sub>s</sub> /2 = 0.318 ·V <sub>s</sub>	V <sub>s</sub> /√2 = 0.707 ·V <sub>s</sub>	V <sub>s</sub> /√2 = 0.707 ·V <sub>s</sub>
Form Factor - FF	1.57	1.11	1.11
Rectification Ratio - η	0.405	0.81	0.81
Ripple Factor - RF	1.21	0.482	0.482
Transformer Utilization Factor - TUF	0.286	0.572	0.81
Diode Peak Inverse Voltage (PIV) - V <sub>RRM</sub>	$V_s = \pi \cdot V_{DC}$	$2 \cdot V_s = \pi \cdot V_{DC}$	$V_s = \pi/2 \cdot V_{DC}$
Peak Direct Voltage (PDV - thyristors only) - V <sub>DRM</sub>	$V_s = \pi \cdot V_{DC}$	$2 \cdot V_s = \pi \cdot V_{DC}$	$V_s = \pi/2 \cdot V_{DC}$
Diode Peak Forward Current - I <sub>FRM</sub>	$\pi \cdot \mathbf{I}_{DC}$	$\pi/2 \cdot \mathbf{I}_{DC}$	$\pi/2 \cdot I_{DC}$
Diode Average Current - I <sub>F(AV)</sub>	I <sub>DC</sub>	0.5 · I <sub>DC</sub>	0.5 · I <sub>DC</sub>
Diode Rms Current - IF(RMS)	π/2 · I <sub>DC</sub>	π/4 · I <sub>DC</sub>	$\pi/4 \cdot I_{DC}$
Fundamental Ripple Frequency - <b>f</b> <sub>R</sub>	f <sub>mains</sub>	2 · f <sub>mains</sub>	2 · f <sub>mains</sub>









#### **Waveforms for the Full-Wave Bridge**

